

Processor 305
Bus Control 310
Memory 315
I/O 320
Short Range Wireless Baseband Controller 330
Transceiver 335

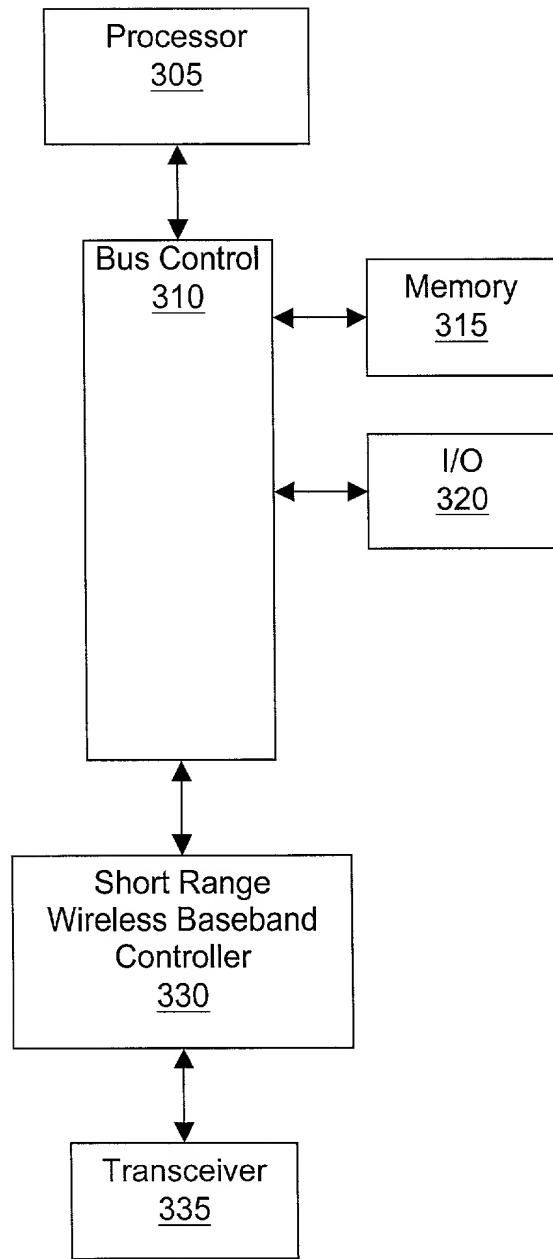


Figure 1a

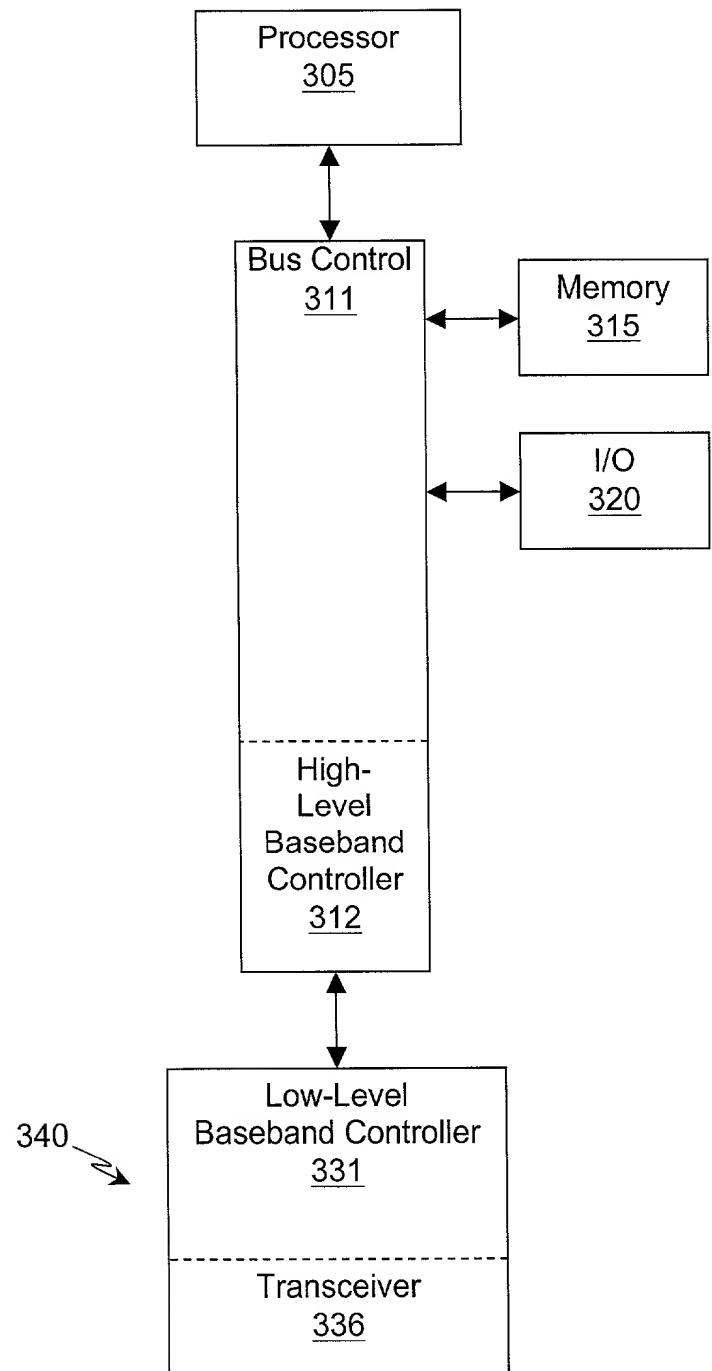


Figure 1b

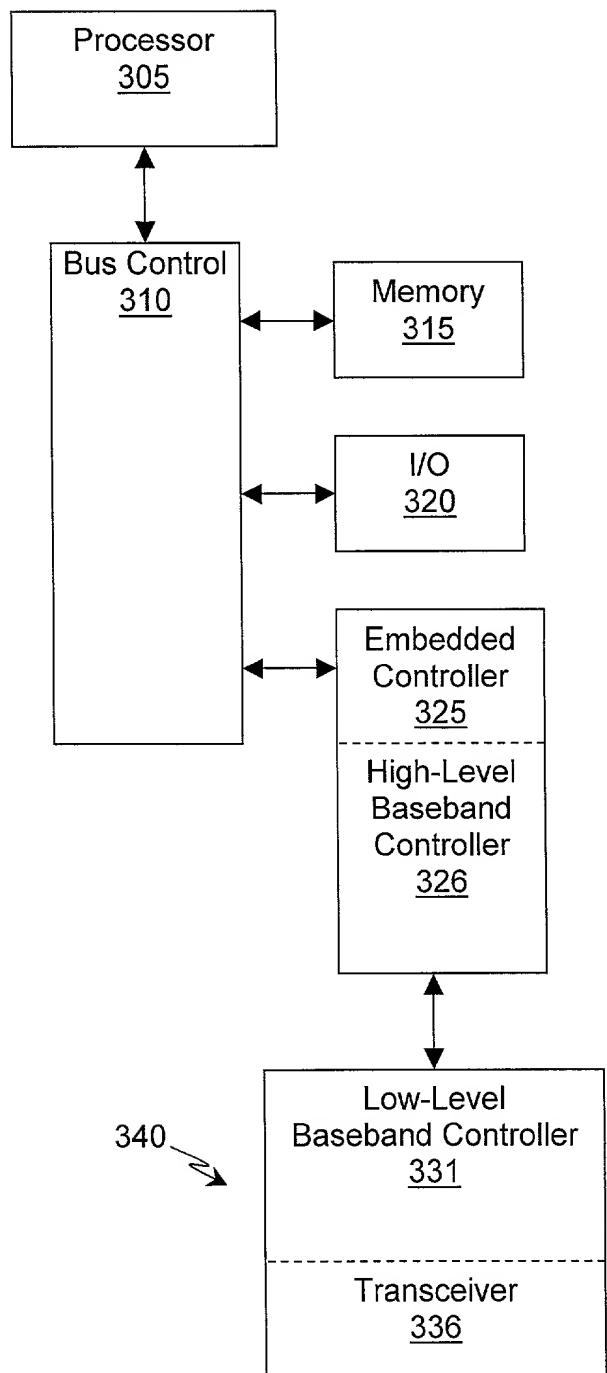


Figure 1c

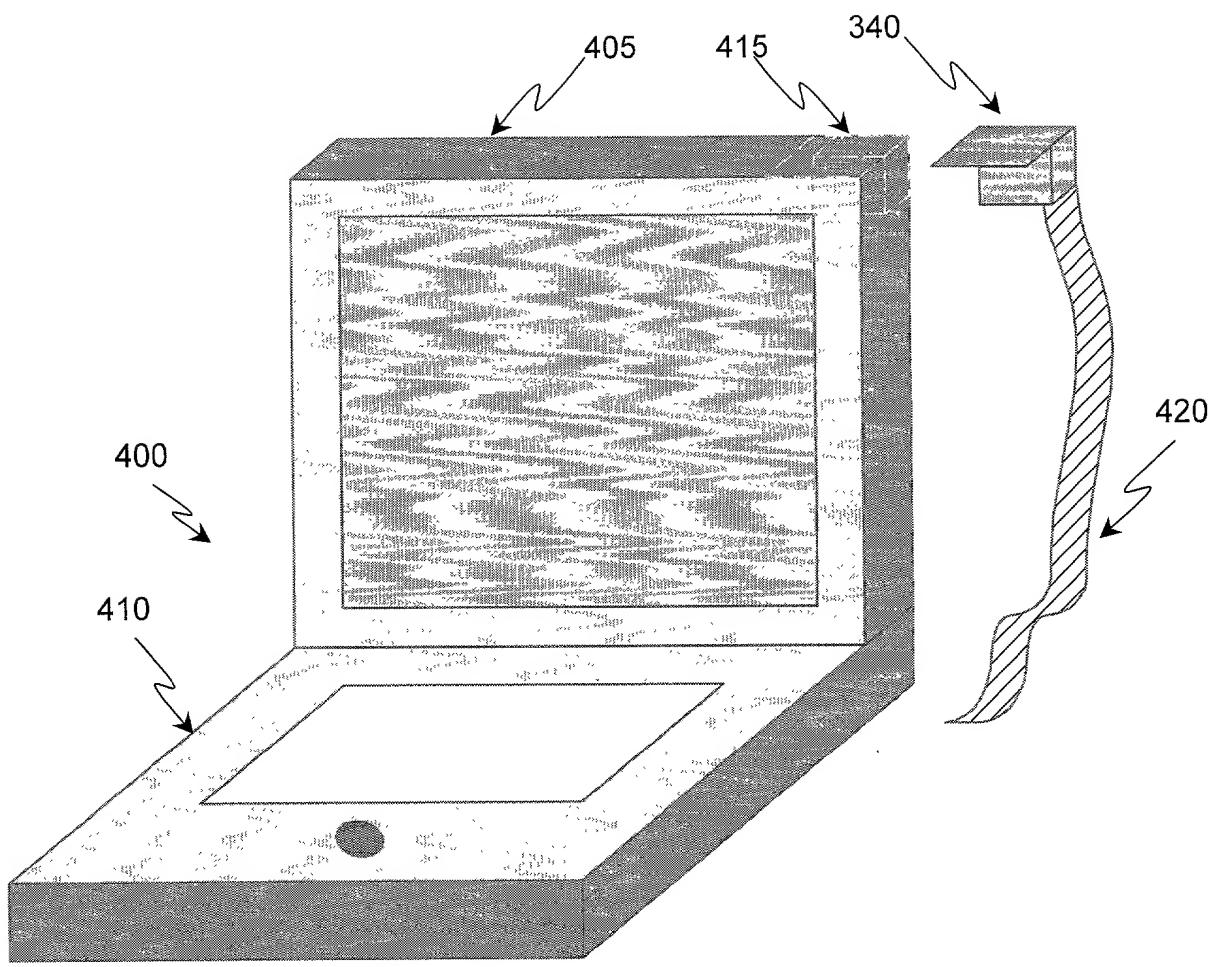


Figure 1d

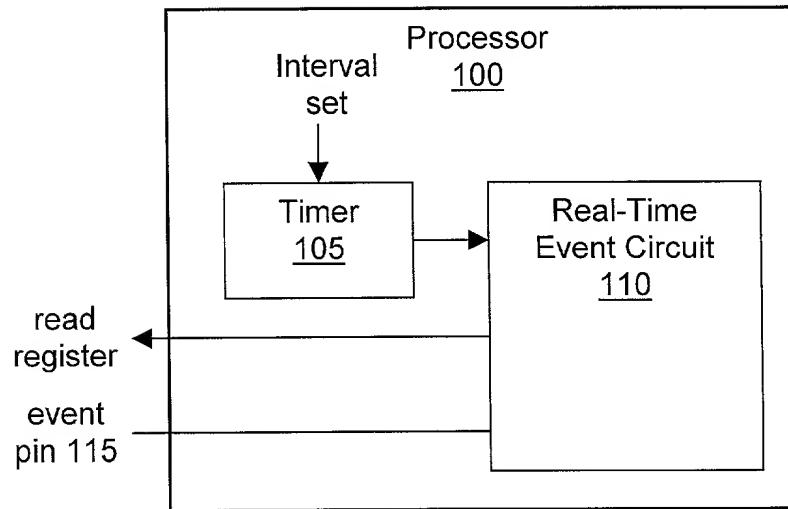


Figure 2a

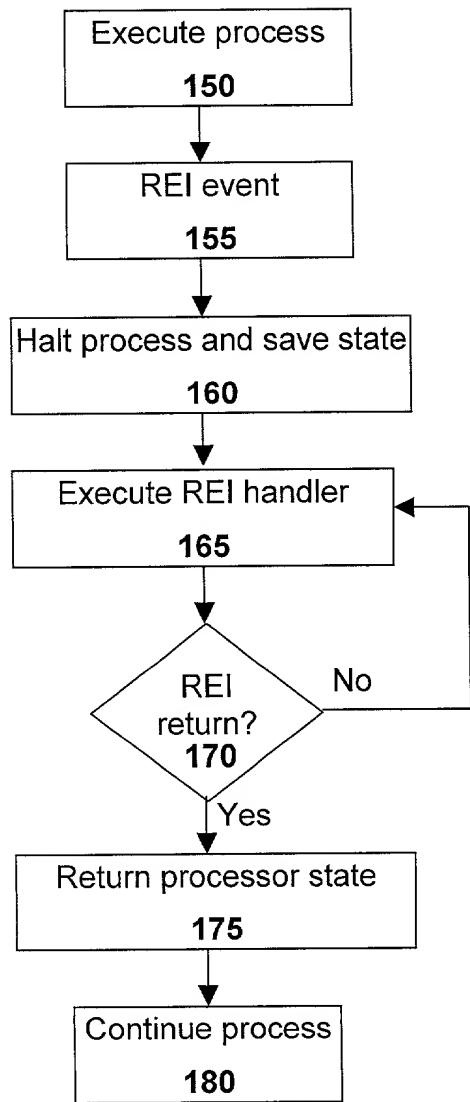


Figure 2b

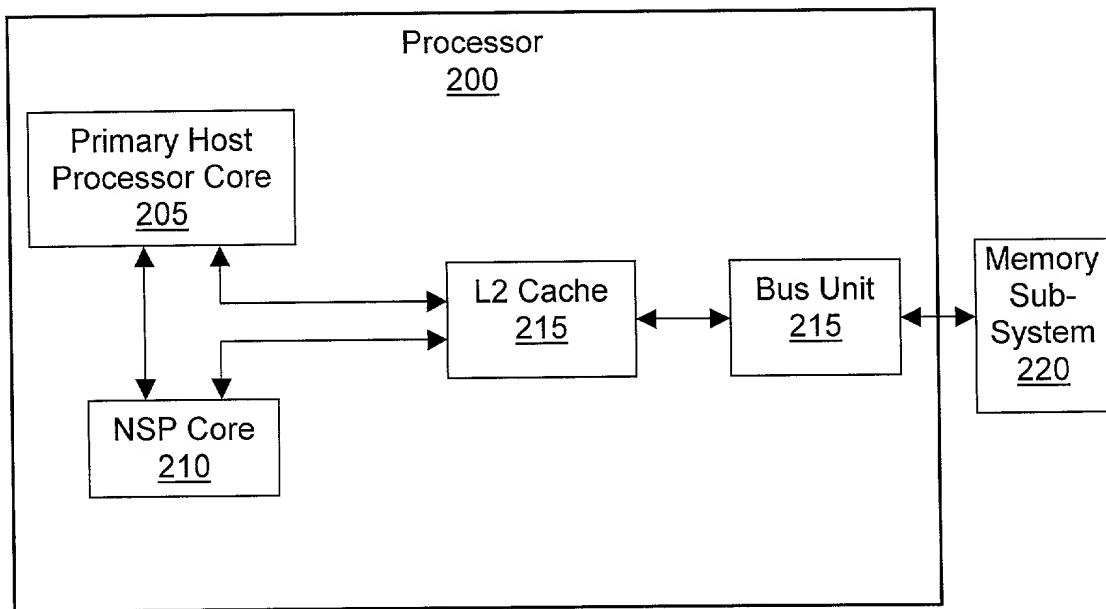


Figure 3